AMENDMENTS TO THE CLAIMS

Please amend claims the claims as shown below. A complete listing of all pending claims is presented.

1. (Cancelled)

2. (Cancelled)

3. (Cancelled)

4. (Cancelled)

5. (Cancelled)

6. (Cancelled)

7. (Cancelled)

8. (Cancelled)

9. (Cancelled)

10. (Cancelled)

11. (Currently amended) A thin film semiconductor device comprising a semiconductor thin film, a gate insulating film accumulated on one surface thereof, and a gate electrode accumulated entirely within a prescribed region of said semiconductor thin film through said gate insulating thin film,

wherein said semiconductor thin film includes polycrystalline silicon having a first particle diameter, wherein said polycrystalline silicon is an irradiation converted substrate that in the prescribed region has a 30 to 80 nm layer of amorphous silicon or polycrystalline silicon having a second particle diameter that is [smaller] larger than said first particle diameter;

a thin film transistor integrated in said prescribed region through said semiconductor thin film, wherein said converted polycrystalline silicon semiconductor film has a single-shot irradiated region, and

a cross sectional shape of said energy beam is adjusted with respect to said prescribed region to consist of irradiating said prescribed region in its entirety at a time by a single shot irradiation, so that characteristics of said thin film transistor are made uniform; and

whereby said single-shot irradiated region is a borderless irradiated region; and

wherein [said film forming step and said irradiating step are alternately repeated] wherein said semiconductor thin film is accumulated without exposing said substrate to air to accumulate said semiconductor thin film.

12. (Currently amended) A display device comprising a pair of substrates adhered to each other with a prescribed gap, and an electrooptical substance maintained in said gap, one of said substrates comprises a counter electrode, the other substrate comprises a pixel electrode and a thin film transistor driving said pixel electrode, and said thin film transistor comprises a semiconductor thin film and a gate electrode accumulated entirely within a prescribed region of one surface of said semiconductor thin film through a gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon having a first particle diameter, wherein said polycrystalline silicon is an irradiation converted substrate that in the prescribed region has a 30 to 80 nm layer of amorphous silicon or polycrystalline silicon having a second particle diameter that is [smaller] larger than said first particle diameter;

a thin film transistor integrated in said prescribed region through said semiconductor thin film wherein said converted polycrystalline silicon semiconductor film has a single-shot irradiated region; and

a cross sectional shape of said energy beam is adjusted with respect to said prescribed region to consist of irradiating said prescribed region in its entirety at a time by a single shot irradiation, so that characteristics of said thin film transistor are made uniform; and

whereby said single-shot irradiated region is a borderless irradiated region; and

wherein [said film forming step and said irradiating step are alternately repeated without exposing said substrate to air, to accumulate] said semiconductor thin film is accumulated without exposing said substrate to air to accumulate said semiconductor thin film.

13. (Cancelled)

14. (Cancelled)

15. (Cancelled)

16. (Cancelled)

17. (Currently amended) A thin film semiconductor device comprising a semiconductor thin film, a gate insulating film accumulated on one surface thereof, and a gate electrode accumulated entirely within a unit of said semiconductor thin film through said gate insulating thin film,

wherein said semiconductor thin film includes polycrystalline silicon having a first particle diameter, wherein said polycrystalline silicon is an irradiation converted substrate having a 30 to 80 nm layer of amorphous silicon or polycrystalline silicon having a second particle diameter that is [smaller] larger than said first particle diameter,

wherein at least one unit of the semiconductor thin film is a single-shot irradiated unit based on a cross sectional shape of said energy beam, and

a thin film transistor is integrated and formed in said [units] at least one unit thus subjected to irradiation at a time; and

whereby said irradiated region is a borderless irradiated region; and

whereby said film [forming step and said irradiating step are alternately repeated] is accumulated without exposing said substrate to air, to accumulate said semiconductor thin film

18. (Currently amended) A display device comprising a pair of substrates adhered to each other with a prescribed gap, and an electrooptical substance maintained in said gap, one of said substrate comprises a counter electrode, the other substrate comprises a pixel electrode and a thin film transistor driving said pixel electrode, and said thin film transistor comprises a semiconductor thin film and a gate electrode accumulated entirely within a unit of one surface of said semiconductor thin film through a gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon having a first particle diameter, wherein said polycrystalline silicon is an irradiation converted 30 to 80 nm layer of amorphous silicon or polycrystalline silicon having a second particle diameter that is [smaller] larger than said first particle diameter,

wherein at least one unit of the semiconductor thin film is a single-shot irradiated unit, based on a cross sectional shape of said energy beam, and

a thin film transistor is integrated and formed in said [units] at least one unit thus subjected to irradiation at a time; and

whereby said irradiated region is a borderless irradiated region; and

wherein said film [forming step and said irradiating step are alternately repeated] is accumulated without exposing said substrate to air, to accumulate said semiconductor thin film.

19. (Cancelled)

20. (Cancelled)

21. (Cancelled)

22. (Cancelled)

23. (Cancelled)

24. (Cancelled)

25. (Cancelled)

26. (Cancelled)

27. (Currently amended) A thin film transistor having a laminated structure comprising a semiconductor thin film, a gate insulating film accumulated on one surface thereof, and a gate electrode accumulated entirely within a prescribed region of said semiconductor thin film through said gate insulating thin film,

wherein said semiconductor thin film includes polycrystalline silicon having a first particle diameter, wherein in the prescribed region said polycrystalline silicon has an irradiation converted 30 to 80 nm layer of amorphous silicon or polycrystalline silicon having a second particle diameter that is [smaller] larger than said first particle diameter, and

said semiconductor thin film is accumulated [by alternately repeating said film forming step and said irradiation step] without exposing said substrate to the air; and

whereby said irradiated region is a borderless irradiated region.

28. (Currently amended) A display device comprising a pair of substrates adhered to each other with a prescribed gap, and an electrooptical substance maintained in said gap, one of said substrate comprises a counter electrode, the other substrate comprises a pixel electrode and a thin film transistor driving said pixel electrode, and said thin film transistor comprises a

semiconductor thin film and a gate electrode accumulated entirely within a prescribed region of one surface of said semiconductor thin film through a gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon having a first particle diameter, wherein said polycrystalline silicon is an irradiation converted substrate that in the prescribed region has a 30 to 80 nm layer of amorphous silicon or polycrystalline silicon having a second particle diameter that is [smaller] larger than said first particle diameter; and

said semiconductor thin film is accumulated by alternately repeating said film forming step, where each additional formed film is about 1 nm, and said irradiation step without exposing said substrate to the air; and

whereby said irradiated region is a borderless irradiated region.

29. (Cancelled)

30. (Cancelled)

31. (Cancelled)

32. (Cancelled)

33. (Cancelled)

34. (Cancelled)

35. (Cancelled)

36. (Cancelled)

37. (Cancelled)

38. (Cancelled)

39. (Previously presented) A thin film transistor having a laminated structure comprising a semiconductor thin film, a gate insulating film accumulated on one surface thereof, and a gate electrode accumulated entirely within a prescribed region of said semiconductor thin film through said gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon, wherein said polycrystalline silicon is an irradiation converted substrate having a 30 to 80 nm layer of non-single crystal silicon, said converted polycrystalline silicon corresponds to a cross-sectional area of the substrate irradiated with pulse laser light having an emission time width from upstand to downfall of at least 50ns, and

a desired change to said energy intensity of said laser light from upstand to downfall of said pulse is applied to said polycrystalline silicon; and

whereby said irradiated region is a borderless irradiated region; and

wherein [said film forming step and said irradiating step are alternately repeated without exposing said substrate to air, to accumulate] said semiconductor thin film is accumulated without exposing said substrate to air.

40. (Currently amended) A display device comprising a pair of substrates adhered to each other with a prescribed gap, and an electrooptical substance maintained in said gap, one of said substrate comprises a counter electrode, the other substrate comprises a pixel electrode and a thin film transistor driving said pixel electrode, and said thin film transistor comprises a semiconductor thin film and a gate electrode accumulated entirely within a prescribed region of one surface of said semiconductor thin film through a gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon, wherein said polycrystalline silicon is an irradiation converted substrate having a 30 to 80 nm layer of non-single crystal silicon, said converted polycrystalline silicon corresponds to a cross-sectional area of the substrate irradiated with pulse laser light having an emission time width from upstand to downfall of at least 50ns,

a desired change to said energy intensity of said laser light from upstand to downfall of said pulse is applied to said polycrystalline silicon; and

whereby said irradiated region is a borderless irradiated region; and

wherein said film [forming step and said irradiating step are alternately repeated] is accumulated without exposing said substrate to air, to accumulate said semiconductor thin film.

41. (Cancelled)

42. (Cancelled)

43. (Cancelled)

44. (Cancelled)

45. (Cancelled)

46. (Cancelled)

47. (Cancelled)

48. (Cancelled)

49. (Cancelled)

50. (Cancelled)

51. (Cancelled)

52. (Cancelled)

53. (Currently amended) A thin film transistor having a laminated structure comprising a semiconductor thin film, a gate insulating film accumulated on one surface thereof, and a gate electrode accumulated entirely within a prescribed region of said semiconductor thin film through said gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon, wherein said polycrystalline silicon is an irradiation converted substrate having a 30 to 80 nm layer of non-single crystal silicon, said converted polycrystalline silicon corresponds to a cross-sectional area of the substrate in the prescribed region that is irradiated with pulse laser light having an emission time width of at least 50ns in a non-oxidative atmosphere,

whereby said irradiated region is a borderless irradiated region; and

wherein [said film forming step and said irradiating step are alternately repeated without exposing said substrate to air, to accumulate] said semiconductor thin film is accumulated without exposing said substrate to air.

54. (Currently amended) A display device comprising a pair of substrates adhered to each other with a prescribed gap, and an electrooptical substance maintained in said gap, one of said substrate comprises a counter electrode, the other substrate comprises a pixel electrode and a thin film transistor driving said pixel electrode, and said thin film transistor comprises a semiconductor thin film and a gate electrode accumulated entirely within a prescribed region of one surface of said semiconductor thin film through a gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon, wherein said polycrystalline silicon is an irradiation converted substrate having a 30 to 80 nm layer of non-single crystal silicon, said converted polycrystalline silicon corresponds to a cross-sectional area of the substrate in the prescribed region that is irradiated with pulse laser light having an emission time width of at least 50ns,

whereby said irradiated region is a borderless irradiated region; and

wherein said film [forming step and said irradiating step are alternately repeated] is accumulated without exposing said substrate to air, to accumulate said semiconductor thin film.

55. (Cancelled)

56. (Cancelled)

57. (Cancelled)

58. (Cancelled)

59. (Cancelled)

60. (Cancelled)

61. (Cancelled)

62. (Cancelled)

63. (Currently amended) A thin film transistor having a laminated structure comprising a semiconductor thin film, a gate insulating film accumulated on one surface thereof, and a gate electrode accumulated entirely within a prescribed region of said semiconductor thin film through said gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon, wherein said polycrystalline silicon is an irradiation converted substrate having a 30 to 80 nm layer of non-single crystal silicon, said converted polycrystalline silicon corresponds to a cross-sectional area of the substrate in the prescribed region that is irradiated with pulse laser light having an emission time width of at least 50ns when said substrate is uniformly heated;

whereby said irradiated region is a borderless irradiated region; and

wherein [said film forming step and said irradiating step are alternately repeated without exposing said substrate to air, to accumulate] said semiconductor thin film is accumulated without exposing said substrate to air.

64. (Cancelled)

65. (Currently amended) A display device comprising a pair of substrate adhered to each other with a prescribed gap, and an electrooptical substance maintained in said gap, one of said

substrate comprises a counter electrode, the other substrate comprises a pixel electrode and a thin film transistor comprises a semiconductor thin film and a gate electrode accumulated entirely within a prescribed region of one surface of said semiconductor thin film through a gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon, wherein said polycrystalline silicon is an irradiation converted substrate having a 30 to 80 nm layer of non-single crystal silicon, said converted polycrystalline silicon corresponds to a cross-sectional area of the substrate in the prescribed region that is irradiated with pulse laser light having an emission time width of at least 50ns when said substrate is uniformly heated,

whereby said irradiated region is a borderless irradiated region; and

wherein [said film forming step and said irradiating step are alternately repeated without exposing said substrate to air, to accumulate] said semiconductor thin film is accumulated without exposing said substrate to air.

66. (Cancelled)

67. (Cancelled)

68. (Cancelled)

69. (Cancelled)

70. (Cancelled)

71. (Cancelled)

72. (Cancelled)

73. (Currently amended) A thin film transistor having a laminated structure comprising a semiconductor thin film, a gate insulating film accumulated on one surface thereof, and a gate electrode accumulated entirely within a prescribed region of said semiconductor thin film through said gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon, wherein said polycrystalline silicon is an irradiation converted substrate having a 30 to 80 nm layer of non-single crystal silicon, said converted polycrystalline silicon corresponds to a cross-sectional area of the substrate in the prescribed region that is irradiated with pulse laser light having an

emission time width of at least 50ns when said substrate is cooled to a temperature lower than room temperature,

whereby said irradiated region is a borderless irradiated region; and

wherein said film [forming step and said irradiating step are alternately repeated[is accumulated without exposing said substrate to air, to accumulate said semiconductor thin film.

74. (Currently amended) A display device comprising a pair of substrates adhered to each other with a prescribed gap, and an electrooptical substance maintained in said gap, one of said substrates comprises a counter electrode, the other substrate comprises a pixel electrode and a thin film transistor driving said pixel electrode, and said thin film transistor comprises a semiconductor thin film and a gate electrode accumulated entirely within a prescribed region of one surface of said semiconductor thin film through a gate insulating film,

wherein said semiconductor thin film includes polycrystalline silicon, wherein said polycrystalline silicon is an irradiation converted substrate having a 30 to 80 nm layer of non-single crystal silicon, said converted polycrystalline silicon corresponds to a cross-sectional area of the substrate in the prescribed region that is irradiated with pulse laser light having an emission time width from upstand to downfall of at least 50ns when said substrate is cooled to a temperature lower than room temperature,

whereby said irradiated region is a borderless irradiated region; and

wherein said film [forming step and said irradiating step are alternately repeated] is accumulated without exposing said substrate to air, to accumulate said semiconductor thin film.